

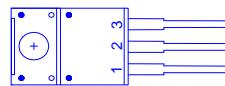
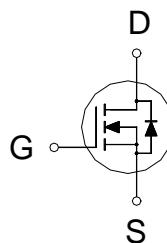
**NIKO-SEM****N-Channel High Voltage Mode  
Field Effect Transistor****P1570VTF**

TO-220F

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
700V	250mΩ	15A

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	700	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current <sup>2</sup>	$I_D$	15	A
		9	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	40	
Avalanche Current <sup>3</sup>	$I_{AS}$	3	A
Avalanche Energy <sup>3</sup>	$E_{AS}$	180	mJ
Power Dissipation	$P_D$	44.6	W
		17.8	
Operating Junction & Storage Temperature Range	$T_j, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.8	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>Ensure that the channel temperature does not exceed 150°C.<sup>3</sup> $V_{DD} = 50\text{V}$ ,  $L = 40\text{mH}$ , starting  $T_J = 25^\circ\text{C}$ .**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	700			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3.4	4	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 30\text{V}$			$\pm 100$	nA
Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 700\text{V}, V_{GS} = 0\text{V}, T_C = 25^\circ\text{C}$			1	$\mu\text{A}$
		$V_{DS} = 560\text{V}, V_{GS} = 0\text{V}, T_C = 100^\circ\text{C}$			100	

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Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 7.5A$	188	250	$m\Omega$
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 10V, I_D = 7.5A$	16		S
<b>DYNAMIC</b>					
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 100V, f = 250KHz$	1523		pF
Output Capacitance	$C_{oss}$		70		
Reverse Transfer Capacitance	$C_{rss}$		6.6		
Effective Output Capacitance <sup>4</sup>	$C_{o(er)}$	$V_{GS} = 0V, V_{DS} = 0 \text{ to } 480V$	56		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DD} = 480V, I_D = 7.5A, V_{GS} = 10V$	43		nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$		7.8		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$		16.6		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DD} = 300V, I_D = 7.5A, R_G = 25\Omega$	36		nS
Rise Time <sup>2</sup>	$t_r$		55		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		142		
Fall Time <sup>2</sup>	$t_f$		50		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b>					
Continuous Current <sup>3</sup>	$I_S$			15	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 7.5A, V_{GS} = 0V$		1	V
Reverse Recovery Time	$t_{rr}$	$I_F = 7.5A, dI_F/dt = 100A / \mu S$	277		nS
Reverse Recovery Charge	$Q_{rr}$		3		uC

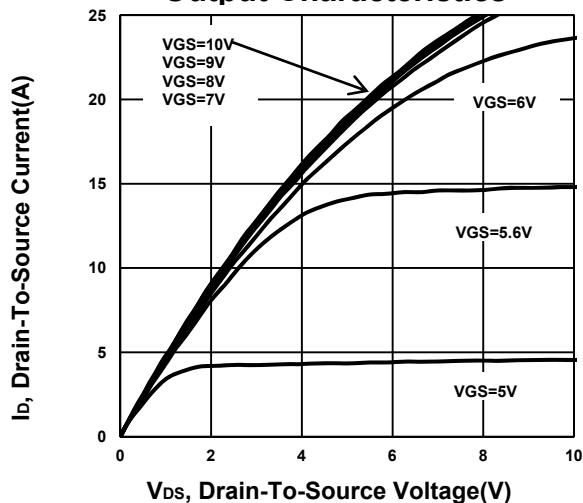
<sup>1</sup>Pulse test : Pulse Width  $\leq 380 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.<sup>3</sup>Pulse width limited by maximum junction temperature.<sup>4</sup> $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{(BR)DSS}$ .

**NIKO-SEM**

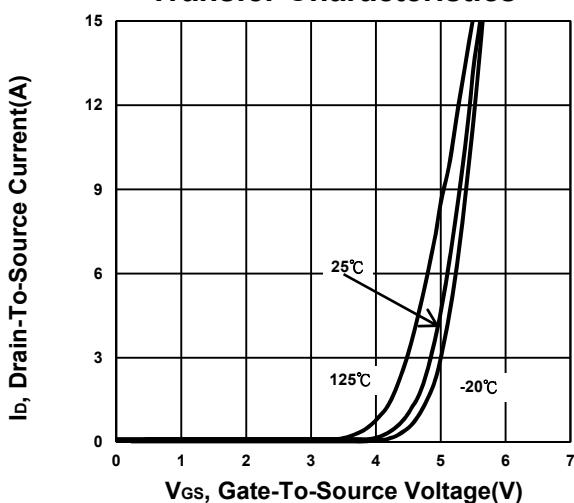
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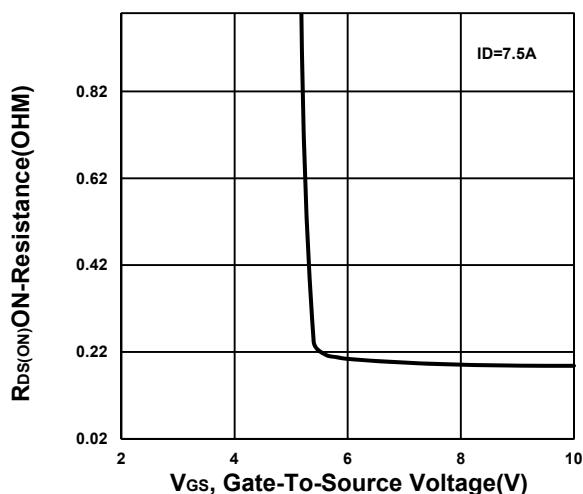
**Output Characteristics**



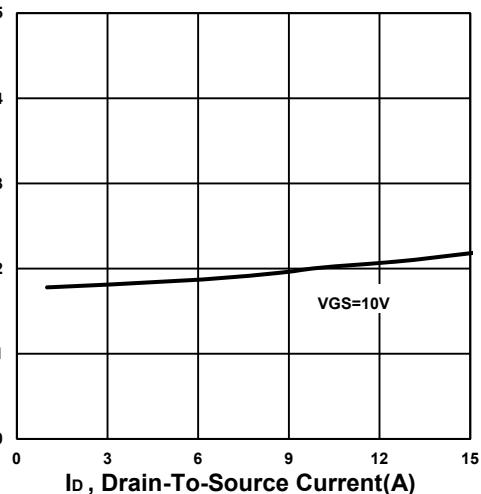
**Transfer Characteristics**



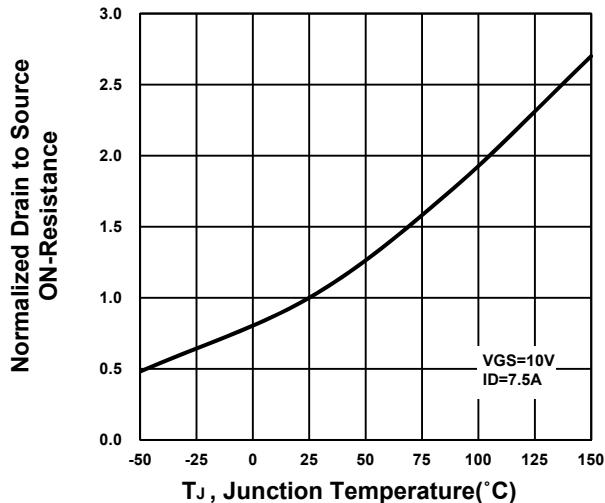
**On-Resistance VS Gate-To-Source**



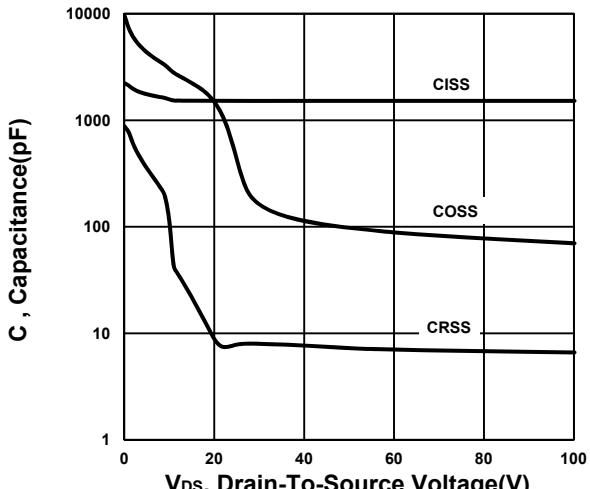
**On-Resistance VS Drain Current**



**On-Resistance VS Temperature**



**Capacitance Characteristic**



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